

2N4338 2N4340
2N4339 2N4341

**SILICON
N-CHANNEL JFETS**



TO-18 CASE



www.centrasemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR 2N4338 Series devices are silicon N-Channel JFETs mounted in a hermetically sealed metal case designed for low level, low noise amplifier applications.

MARKING: FULL PART NUMBER

MAXIMUM RATINGS: ($T_A=25^\circ\text{C}$)

Gate-Drain Voltage
Gate-Source Voltage
Drain-Source Voltage
Gate Current
Power Dissipation
Operating and Storage Junction Temperature

SYMBOL

V_{GD} 50
 V_{GS} 50
 V_{DS} 50
 I_G 50
 P_D 325
 T_J, T_{stg} -65 to +200

UNITS

V
V
V
mA
mW
 $^\circ\text{C}$

ELECTRICAL CHARACTERISTICS: ($T_A=25^\circ\text{C}$ unless otherwise noted)

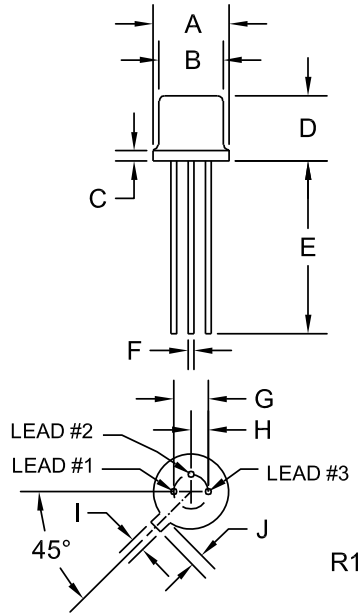
SYMBOL	TEST CONDITIONS	2N4338		2N4339		2N4340		2N4341		UNITS
		MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	
I_{GSS}	$V_{GS}=30\text{V}, V_{DS}=0$	-	0.1	-	0.1	-	0.1	-	0.1	nA
I_{DSS}	$V_{DS}=15\text{V}, V_{GS}=0$	0.2	0.6	0.5	1.5	1.2	3.6	3.0	9.0	mA
BV_{GSS}	$I_G=1.0\mu\text{A}$	50	-	50	-	50	-	50	-	V
$V_{GS(OFF)}$	$V_{DS}=15\text{V}, I_D=0.1\mu\text{A}$	0.3	1.0	0.6	1.8	1.0	3.0	2.0	6.0	V
$ y_{fs} $	$V_{DS}=15\text{V}, f=1.0\text{kHz}$	0.6	1.8	0.8	2.4	1.3	3.0	2.0	4.0	mS
$ y_{os} $	$V_{DS}=15\text{V}, f=1.0\text{kHz}$	-	5.0	-	15	-	30	-	60	μS
C_{iss}	$V_{DS}=15\text{V}, f=1.0\text{MHz}$	-	6.0	-	6.0	-	6.0	-	6.0	pF
C_{rss}	$V_{DS}=15\text{V}, f=1.0\text{MHz}$	-	2.0	-	2.0	-	2.0	-	2.0	pF
NF	$V_{DS}=15\text{V}, f=1.0\text{kHz}, R_G=1.0\text{M}\Omega$	-	1.0	-	1.0	-	1.0	-	1.0	dB

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TO-18 CASE - MECHANICAL OUTLINE



LEAD CODE:

- 1) Source
- 2) Drain
- 3) Gate

MARKING: FULL PART NUMBER

SYMBOL	DIMENSIONS			
	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A (DIA)	0.209	0.230	5.31	5.84
B (DIA)	0.178	0.195	4.52	4.95
C	-	0.030	-	0.76
D	0.170	0.210	4.32	5.33
E	0.500	-	12.70	-
F (DIA)	0.016	0.019	0.41	0.48
G (DIA)	0.100		2.54	
H	0.050		1.27	
I	0.036	0.046	0.91	1.17
J	0.028	0.048	0.71	1.22

TO-18 (REV: R1)

R1 (3-August 2016)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix " TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix " PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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